



DATASHEET CGY2272UH

V – BAND LOW NOISE AMPLIFIER

PRODUCT IN DEVELOPMENT – Engineering Sample Release (ESR)

DESCRIPTION

This V-Band LNA is a high-performance GaAs Low Noise Amplifier MMIC designed to operate in the V-band.

The V-Band LNA is 4 stages Single Supply LNA. It has an exceptionally low noise figure of 2 dB with 25 dB of gain. The on-chip matching provides 15 dB of Input Return Loss and Output Return Loss. Thanks to the DC regulation the gain and noise are very stable wrt temperature change. It can be used in radar, SATCOM, telecommunication and instrumentation applications.

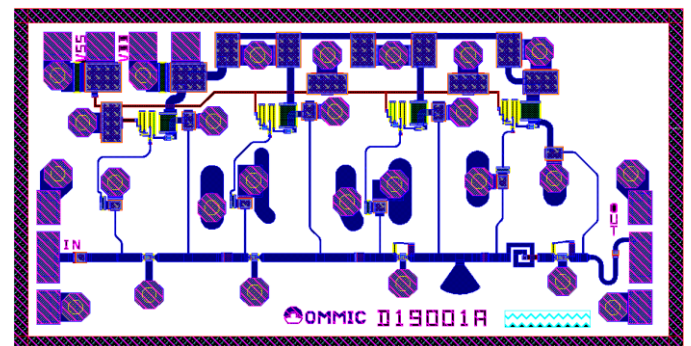
The die is manufactured using OMMIC's Advanced 70 nm gate length high Indium content mHEMT technology. The MMIC uses gold bonding pads and backside metallization and is fully protected with Silicon Nitride passivation to obtain the highest level of reliability.

APPLICATIONS

- Radar
- Telecommunications
- Instrumentation
- Spatial

FEATURES

- **OPERATING RANGE** - 44 – 69 GHz
- **GAIN** 25 dB
- **NOISE FIGURE** < 2 dB
- **Pin max** 15 dBm (CW)
- **POWER CONSUMPTION**
Single positive & negative Supply auto bias / temp controlled
 - VD = 1.5 V
 - VS = 1.5 V
 - I_{Qtot} = 46 mA
- **CHIP SIZE** 2.35 x 1.2 mm²
- **50 Ohms INPUT AND OUTPUT MATCHED**



V – BAND LNA DIE VIEW



ABSOLUTE MAXIMUM RATING

 $T_{amb} = + 25 \text{ }^{\circ}\text{C}$

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
VS	Gate voltage	VD Open circuit	- 3	0	V
VD	Drain voltage	VD Open circuit	0	+ 3	V
Is / ID	Current			5/90	mA
PIN	RF input power	CW		+ 15	dBm
T _{amb}	Ambient temperature		- 40	+ 85	° C
T _j	Junction temperature			+ 150	° C
T _{stg}	Storage temperature		- 55	+ 150	° C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
Rth _{amb}	Thermal resistance at ambient temperature (+ 20°C)	35.26	° C/W
Rth _{60°C}	Thermal resistance at 85°C	46.54	° C/W

ELECTRICAL CHARACTERISTICS

 $T_{amb} = + 25 \text{ }^{\circ}\text{C}$, $V_d = 1.5 \text{ V}$, $V_s = - 1.5 \text{ V}$ with 0.15 nH equivalent bondings on both R_{fin} R_{fout}

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
RFin	Input frequency		44		69	GHz
Performances on Reference Board with 0.25 nH bonding parasitic inductor at input and output						
VD	Drain Supply Voltage		+ 1.3	+ 1.5	+ 1.7	V
Is + ID	Drain Supply Current	@ Vd = 1.5		46		mA
G	Gain		23	25	26.5	dB
NF	Noise Figure				2	dB
OP1dB	1dB compression point			1.8		dBm
ISO _{rev}	Reverse Isolation	R _{FOUT} /R _{FIN}	-50		- 32	dB
S ₁₁	Input reflection coefficient	50 Ohms			- 10	dB
S ₂₂	Output reflection coefficient	50 Ohms			- 10	dB





ON WAFER

NOISE FIGURE

Conditions : $T_{amb} = + 25^{\circ}\text{C}$ / $V_{DD} = 1.5 \text{ V}$ / $V_{SS} = -1.5 \text{ V}$ / $I_{DD} = 46 \text{ mA}$

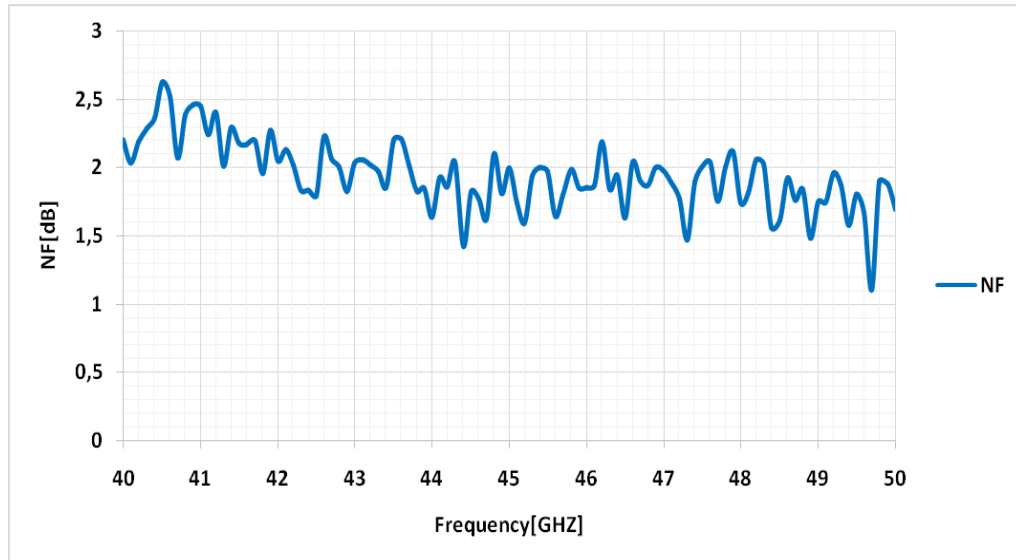


Figure 1: NF vs frequency



ON WAFER

S-PARAMETERS

Conditions : $T_{amb} = + 25^{\circ}\text{C}$ / $V_{DD} = 1.5 \text{ V}$ / $V_{SS} = -1.5 \text{ V}$ / $I_{DD} = 46 \text{ mA}$

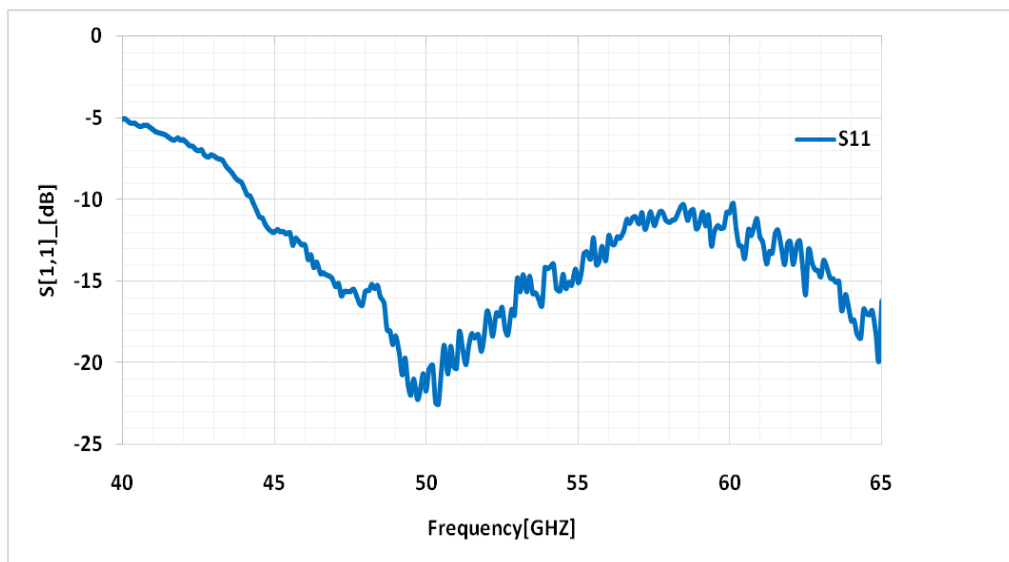


Figure 2: Return loss vs frequency



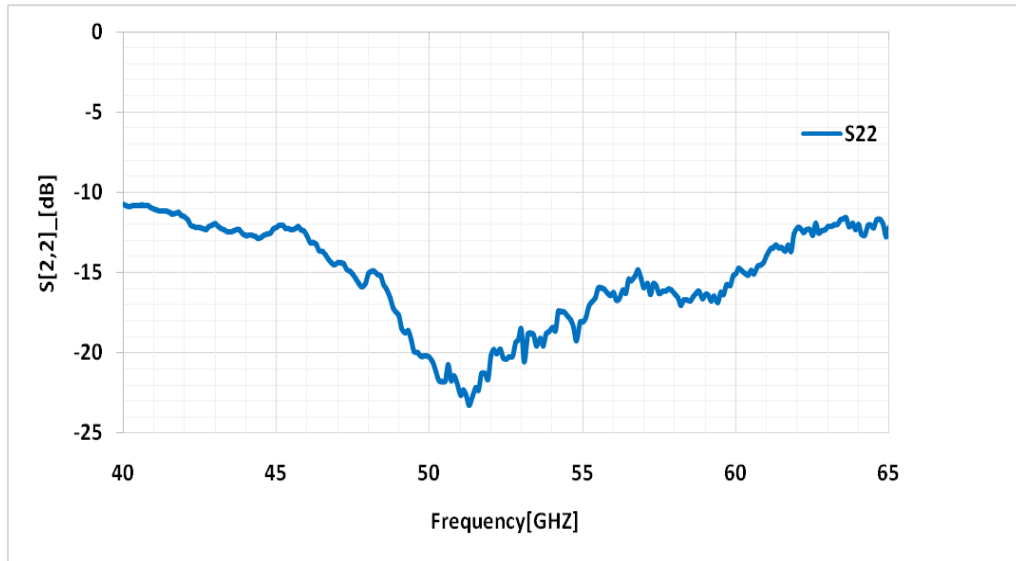


Figure 3: Return loss vs frequency

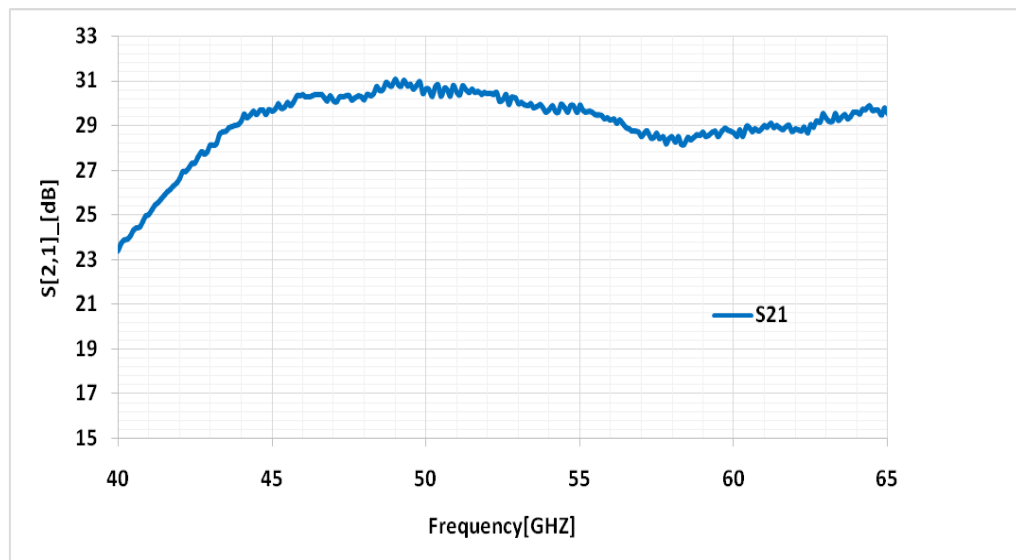


Figure 4: Gain vs frequency





P1dB_IN-OUT

Conditions : $T_{amb} = + 25^{\circ}\text{C}$ / $V_{DD} = 1.5 \text{ V}$ / $V_{SS} = -1.5 \text{ V}$ / $I_{DD} = 46 \text{ mA}$

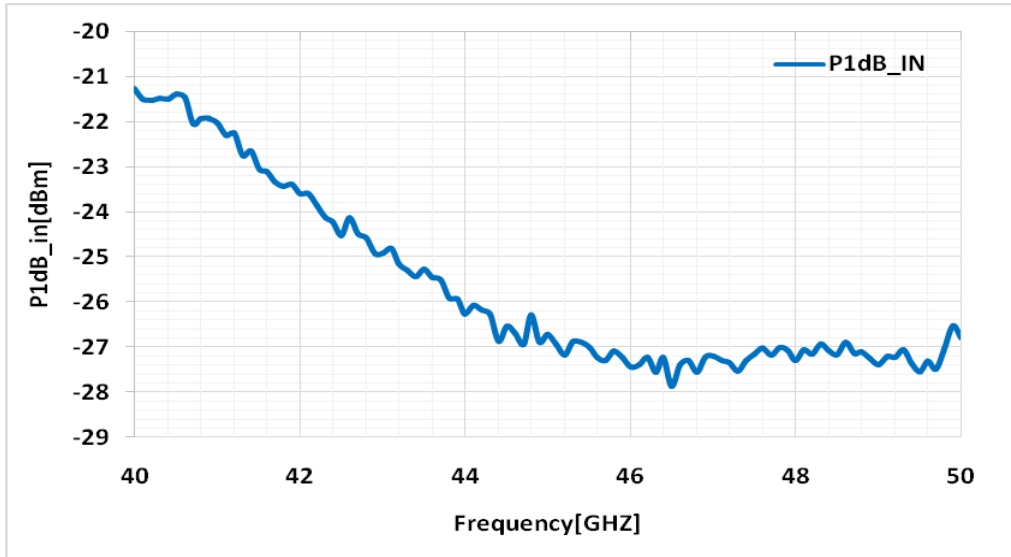


Figure 5: P1dB_IN vs frequency

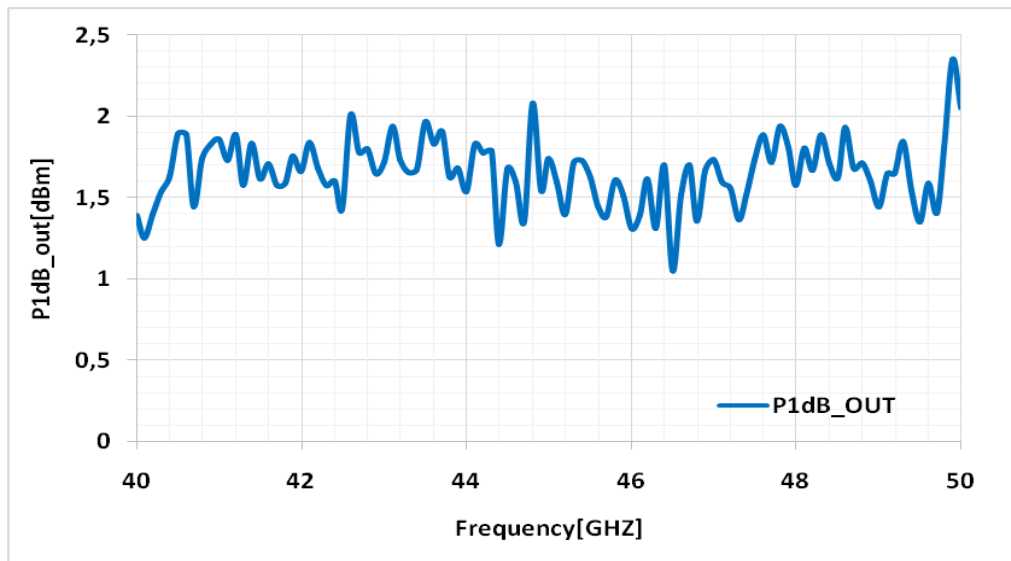


Figure 6: P1dB_OUT vs frequency



APPLICATION SCHEMATIC

Decoupling scheme depends on customer implementation, in order to prevent instability it is highly recommended to place a 100pF RF decoupling chip capacitor at each DC terminal with the shortest possible bonding wires. Additionally, a 1nF SMD capacitor can be added on the DC Pad.

The decoupling network depends on supply, on grounding environment, on form factor, on all parasitics added by the customer environment. According to this, the appropriate network sometimes need to be fine-tuned in accordance with rules applicable in the high frequency domain.

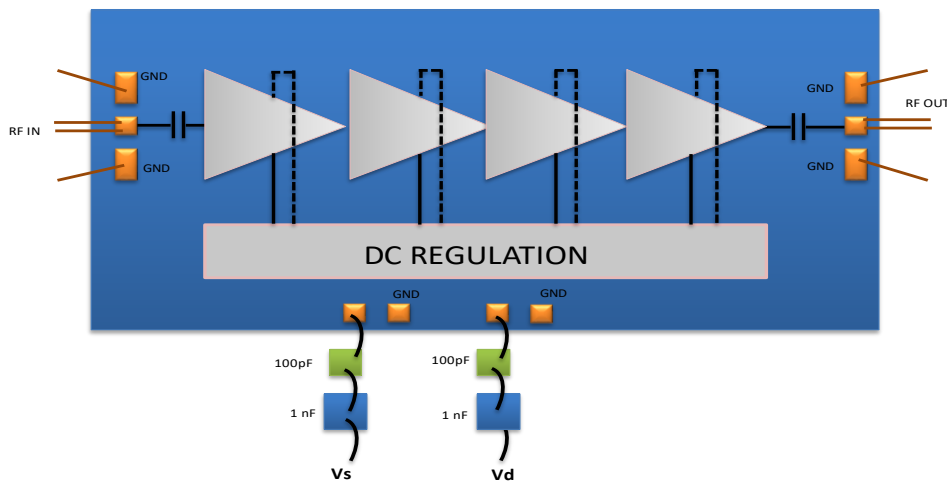


Figure 7: V-Band LNA Application Schematic

PAD DESCRIPTION

SYMBOL	PAD	DESCRIPTION
RFout	OUT	RF output
RFin	IN	RF input
Vd	VD	Positive supply voltage
Vs	Vs	Negative supply voltage
GND	BACKSIDE	Ground

Note: In order to ensure good RF performances and stability It is key to connected to the ground the pad available on the backside of the die.

PAD LAYOUT

VDD, VSS are DC signals applied on the north side. Many ground accesses are complementing the pad layout. The backside is the ground reference plan.

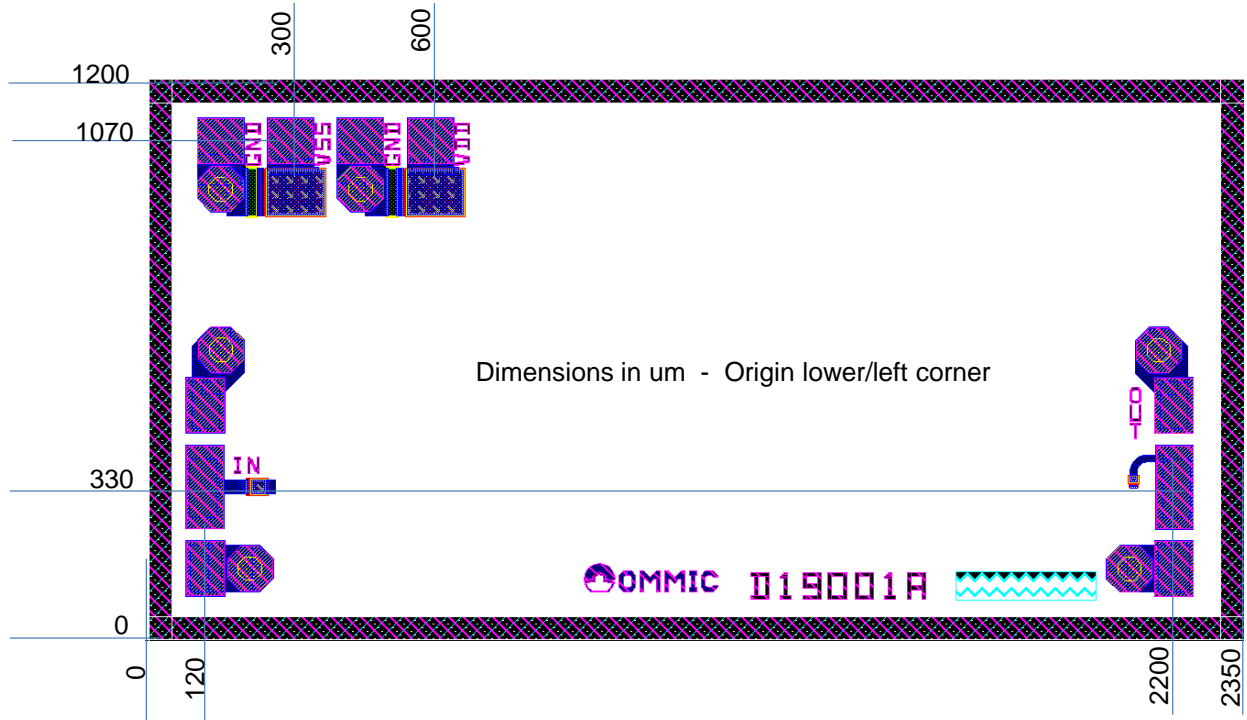


Figure 8: V-Band LNA PAD Layout

DEFINITIONS

LIMITING VALUES DEFINITION

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

APPLICATION INFORMATION

Applications that are described herein for any of these products are for illustrative purposes only. OMMIC makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

DISCLAIMERS

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. OMMIC's customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify OMMIC for any damages resulting from such application.

RIGHT TO MAKE CHANGES

OMMIC reserves the right to make changes, without notice, in the products, including circuits, standard cells, and/or software, described or contained herein in order to improve design and/or performance. OMMIC assumes no responsibility or liability for the use of any of these products, conveys no licence or title under any patent, copyright, or mask work right to these products, and makes no representations or warranties that these products are free from patent, copyright, or mask work right infringement, unless otherwise specified.

