

Advance Information

Ka-Band GaN LNA

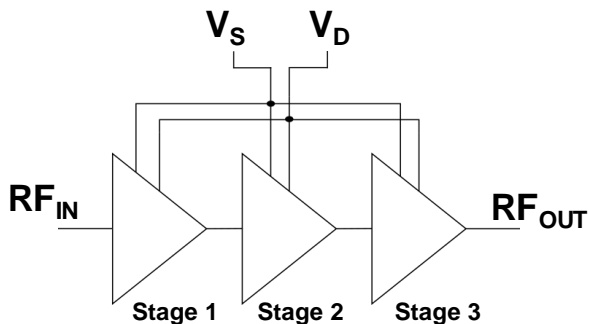
26 – 34 GHz 1.6 dB NF Low Noise Amplifier

Description

This Ka-Band LNA is a high-performance GaN Low Noise Amplifier MMIC designed to operate in the Ka-band.

The Ka-Band LNA has 1.6 dB of noise figure and 20 dB of output forward gain.

The performances of the Ka-Band LNA make it well suited to be used in Radar, Telecommunication and Space applications. This technology is being evaluated for space applications.



Ka-Band LNA Power Amplifier block diagram

Application

- ▶ Radar
- ▶ Spatial
- ▶ Telecommunication

Features

- ▶ Operating Range: 26 GHz to 34 GHz
- ▶ Gain: 20 dB
- ▶ NF < 1.6 dB
- ▶ Pin_{max} > 30 dBm (pulsed)
- ▶ Source Voltage: V_S = -3 V
- ▶ Power Consumption:
 - V_D = 8.5 V
 - I_{Qtot} = 90 mA
- ▶ Chip size = 1.26 x 3.65 mm²
- ▶ 50 Ohms input and output matched



Ka-Band LNA die view

MAXIMUM VALUES

Symbol	Parameter	Conditions	MIN.	MAX.	UNIT
V _S	Source Voltage				V
V _D	Drain Voltage				V
I _D	Drain Current				mA
P _{IN}	RF Input Power			> 30 (pulsed)	dBm
T _{amb}	Ambient temperature				° C
T _j	Junction temperature				° C
T _{stg}	Storage temperature				° C

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	UNIT
R _{thamb}	Thermal Resistance at ambient temperature (+ 20 °C)	35.26	° C/W
R _{th60°C}	Thermal Resistance at 85 °C	46.54	° C/W

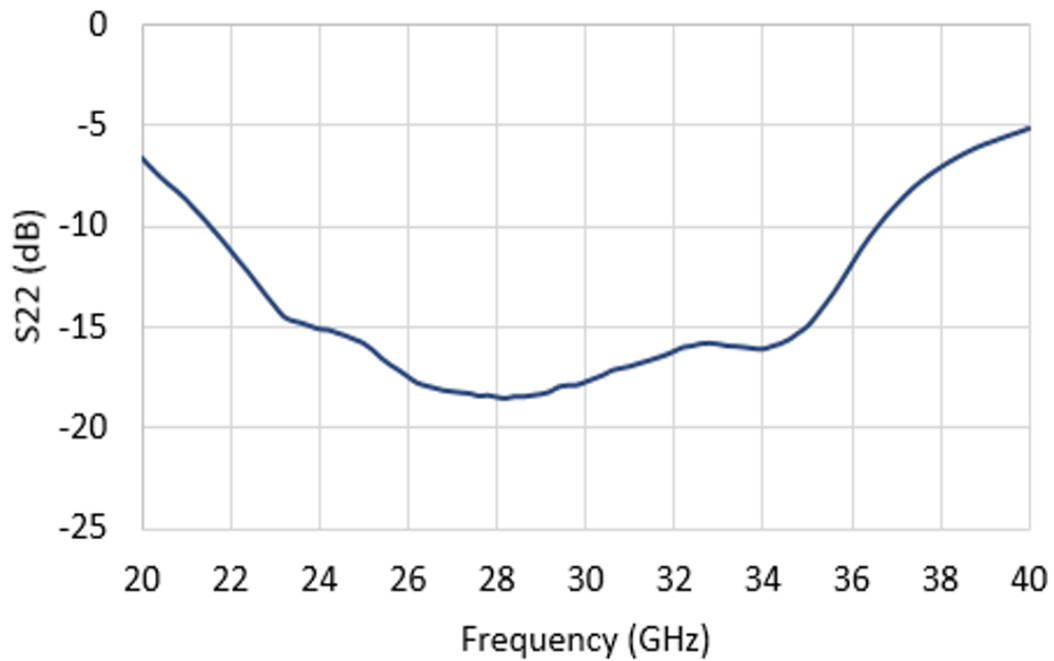
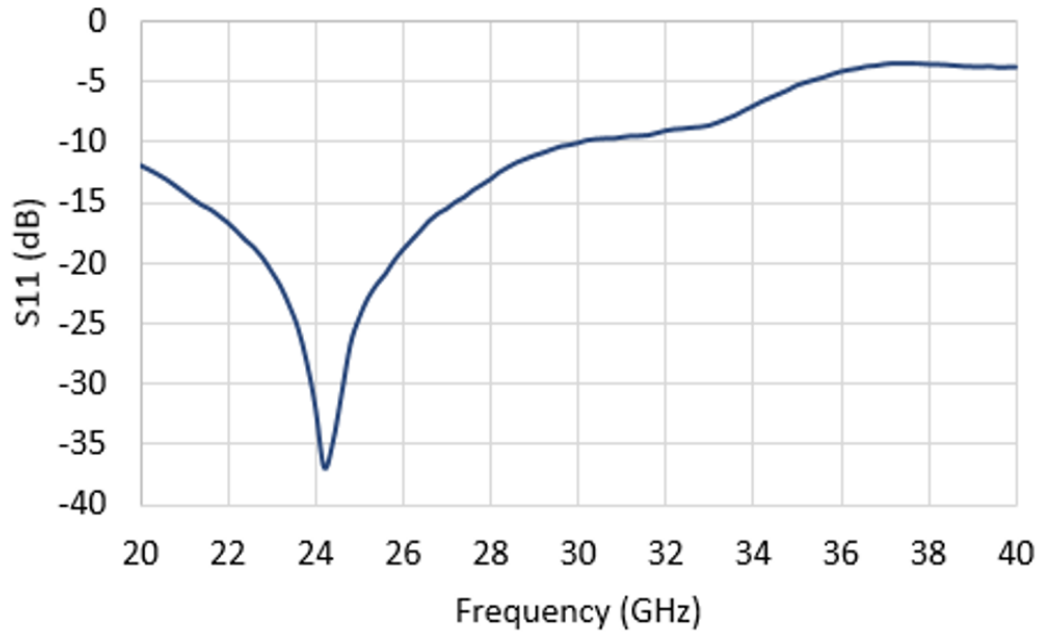
ELECTRICAL CHARACTERISTICS

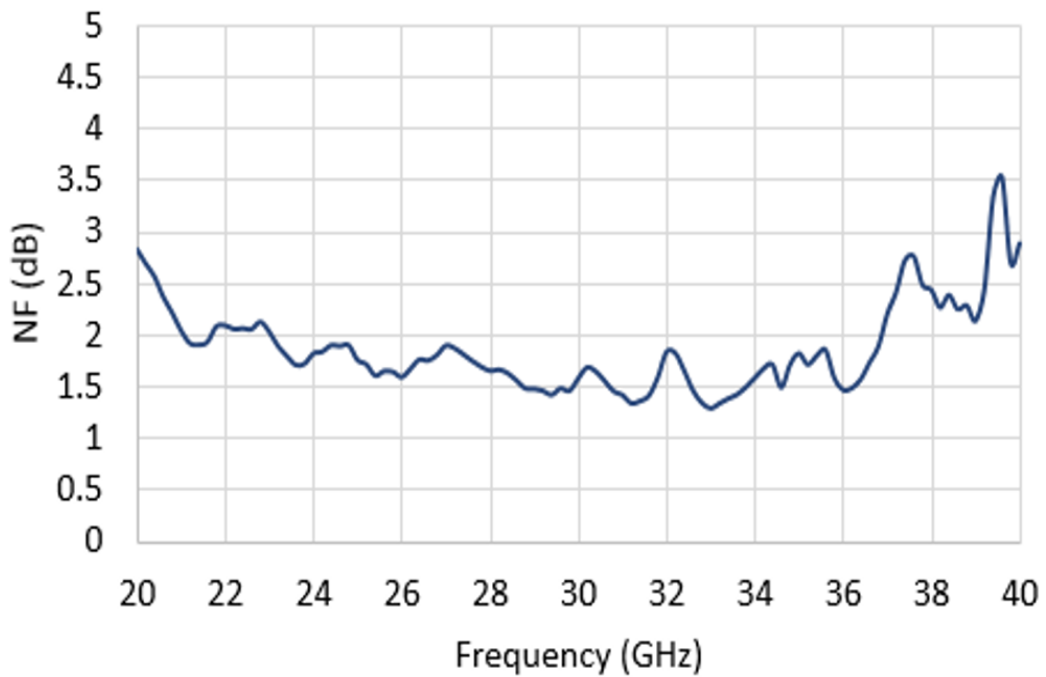
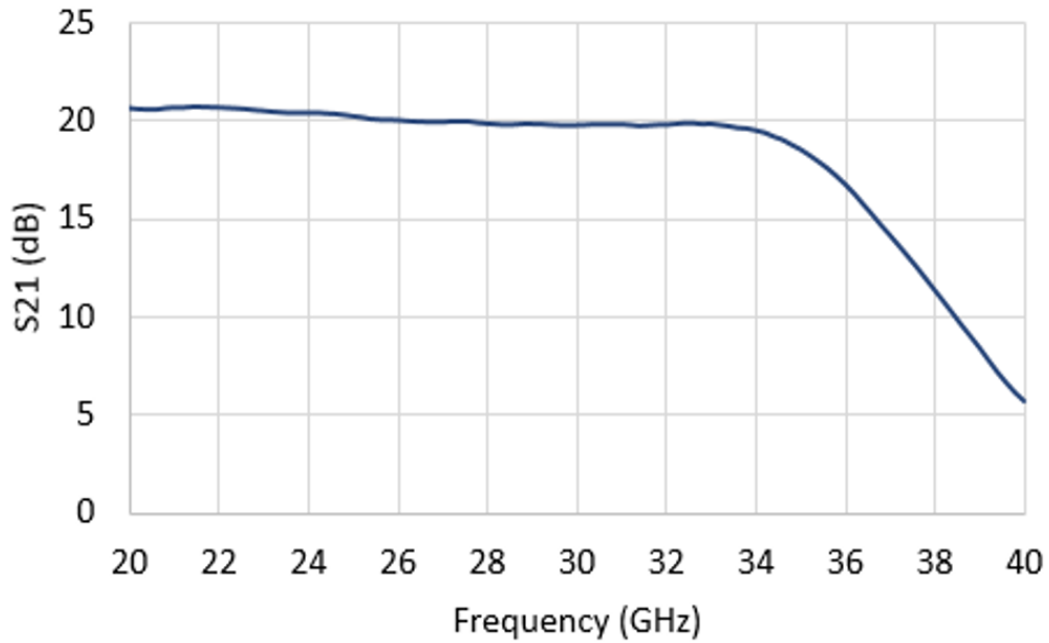
Symbol	Parameter	Conditions	MIN.	TYP.	MAX.	UNIT
RF _{IN}	Input Frequency		26		34	GHz
V _D	Drain Supply Voltage			8.5		V
V _S	Source Supply Voltage			- 3		V
I _D	Total supply current @P _{sat}	Drain Voltage 8 V				mA
G	Gain			20		dB
NF	Noise Figure				1.6	dB
OIP3	Output Third Order Intercept Point			TBD		dBm
S ₁₁	Input Reflexion Coefficient	50 Ohms		- 7		dB
S ₂₂	Output Reflexion Coefficient	50 Ohms		- 15		dB

ON WAFER MEASUREMENTS

Conditions: $T_{amb} = + 25^{\circ}\text{C}$.
 $V_D = 8.5 \text{ V}$; $V_S = -3 \text{ V}$; $I_D = 90 \text{ mA}$

- **S-PARAMETERS**





APPLICATION SCHEMATIC

Decoupling scheme depends on customer implementation, in order to prevent instability it is highly recommended to place a 47pF RF decoupling chip capacitor at each DC terminal with the shortest possible bonding wires. Additionnaly, a 10nF chip capacitor can be added on the drain.

The decoupling network depends on supply, on grounding environement, on form factor, on all parasitics added by the customer environement. According to this, the appropriate network sometimes need to be fine-tuned in accordance with rules applyable in the high frequency domain.

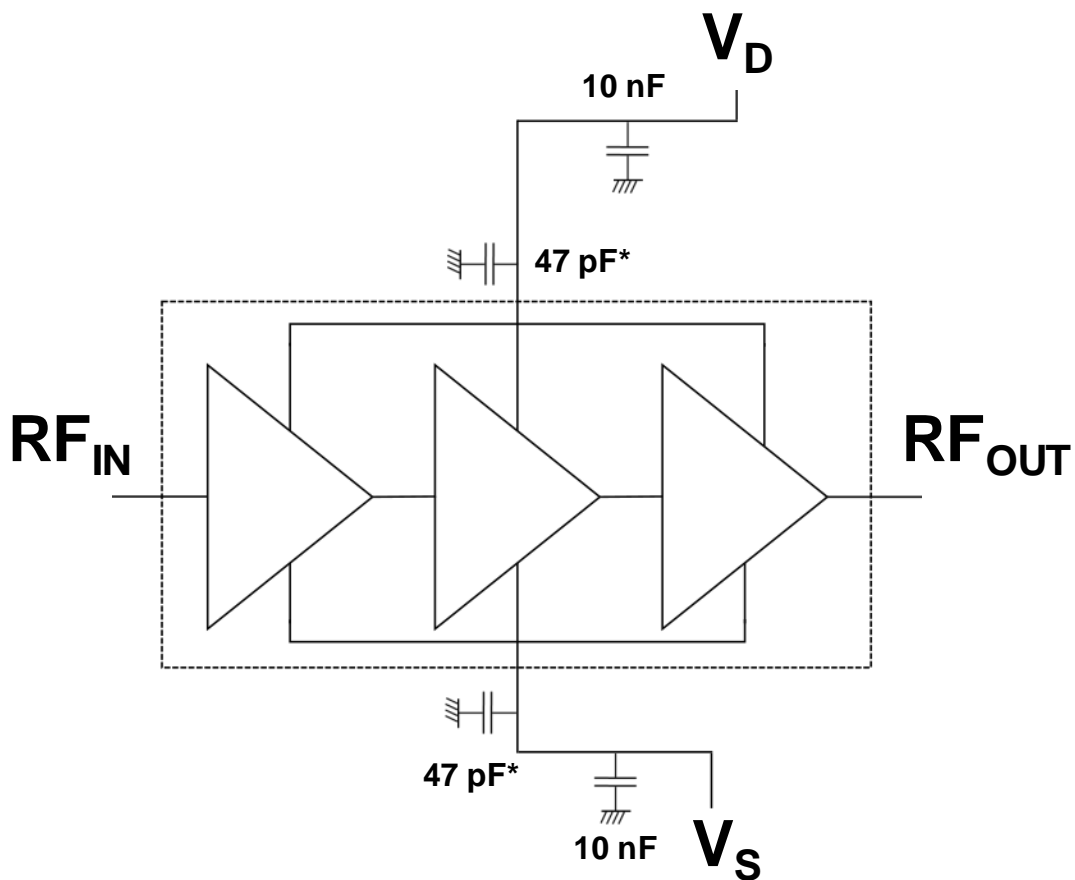


Figure 1 : Ka-Band LNA Application Schematic

PAD LAYOUT

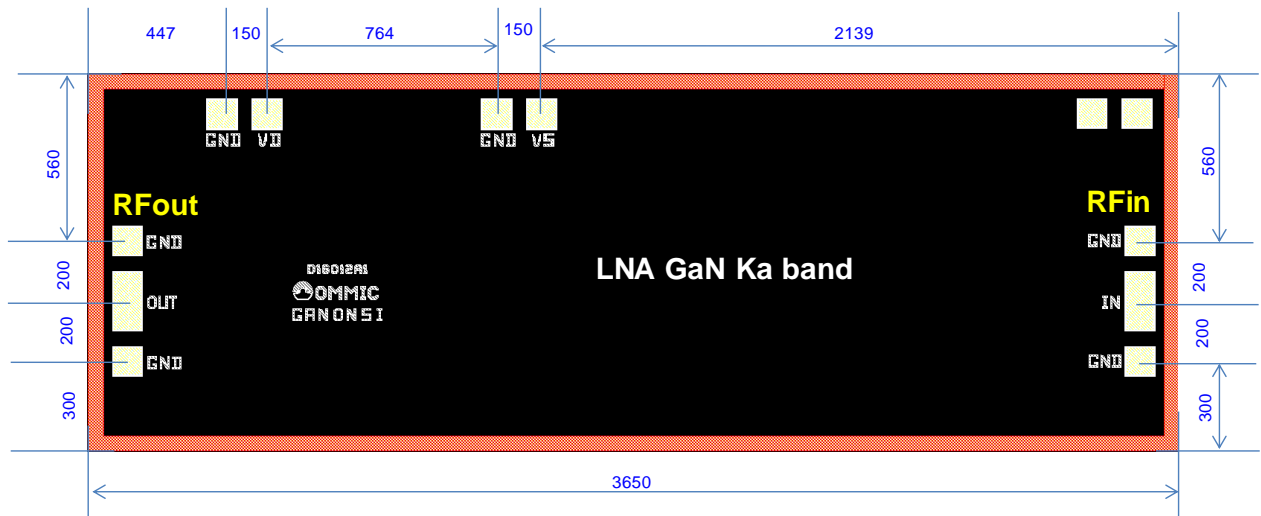


Figure 2: Ka-Band GaN LNA Pad allocation (unit: μm)

DEFINITIONS

Limiting values definition

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Applications that are described herein for any of these products are for illustrative purposes only. OMMIC makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

DISCLAIMERS

Life support applications

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. OMMIC's customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify OMMIC for any damages resulting from such application.

Right to make changes

OMMIC reserves the right to make changes, without notice, in the products, including circuits, standard cells, and/or software, described or contained herein in order to improve design and/or performance. OMMIC assumes no responsibility or liability for the use of any of these products, conveys no licence or title under any patent, copyright, or mask work right to these products, and makes no representations or warranties that these products are free from patent, copyright, or mask work right infringement, unless otherwise specified.