

2 Ports Corechip

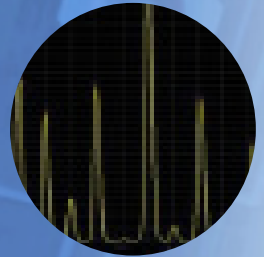
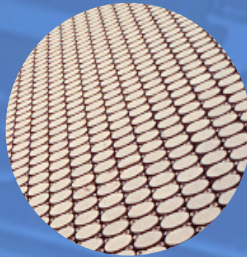
26.5-30.5GHz

CGY2351UH/C1

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The CGY2351UH/C1 is a high performance GaAs MMIC Core Chip operating in Ka-band following the T/R architecture; it is passive and exhibits only 2 RF ports. It includes a 6-bit Phase shifter, and a 5-bit attenuator; It has a phase shift range of 360° with a 5.62° step and attenuation range of 22 dB with 0.4dB step. It operate from 26.5 to 30.5 GHz.



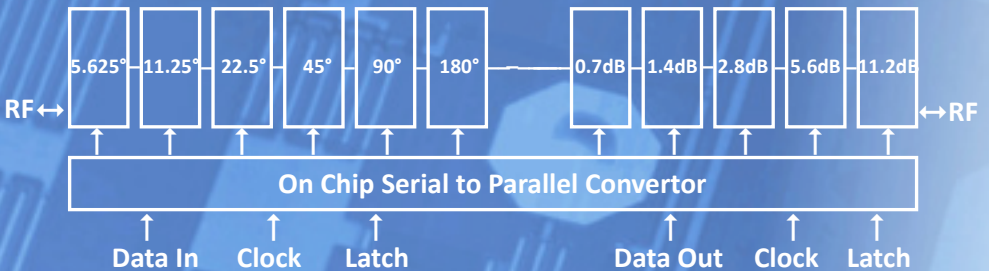
The on-chip control logic with serial input register minimizes the number of bonding pads and greatly simplifies the interfacing to this device. This die is manufactured using OMMIC's $0.18\ \mu\text{m}$ gate length ED02AH PHEMT Technology. The MMIC uses gold bonding pads and backside metallization and is fully protected with Silicon Nitride passivation to obtain the highest level of reliability. This technology has been evaluated by the European Space Agency .

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Features

Operate Frequency Range	From 26.5 to 30.5 GHz
Phase Shifter 6 bits	360 ° max 5.625° step
Attenuation 5 bits	22dB max 0.7dB step
RMS Phase Error	4° all states
RMS Amplitude Error	0.5 dB all states
Input Matching IRL	14dB
Output Matching ORL	13 dB
Supply Voltage	+ 5V & -5V
Total Current Consumption	17 mA
Die Size	4.8 x 1.6 mm



CGY2351UH/C1 Bloc Diagram



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